

**ABSTRACT**

A circuit arrangement and method are disclosed for providing a video signal to a video display from a selectable subset of a plurality of digital video data carried on a plurality of video data channels. The digital video data is generated from video signals from a plurality of video sources, and each video channel selectably carries either color or monochrome video data. A processor is configured and arranged to interpret display commands, and a selector circuit is coupled to the processor. The selector circuit is arranged for connection to the plurality of video data channels and selects digital video data from a subset of the channels responsive to an input selection signal from the processor. A video data sequencer is coupled to the selector circuit and decodes color and monochrome video data from the video channels responsive to configuration signals indicating selected data formats for the channels and merges the selected video data into frames of video data. A digital-to-analog converter generates an analog video signal from the frames of video data.